

JN516xの機能割当

SMD#	DIP#	#PIO	IO名	代表的な機能割当(代替含む)				
1	6	0	DO0	SPICLK			PWM2*	
2	7	5	DO1	SPIMISO			PWM3*	
3	5	2	DIO18	SPIMOSI				
4	8	3	DIO19	SPISEL0				
5	28	VCC	VCC	VCC				
6	9	7	DIO4	CTS0			TIM0OUT	PC0
7	4	6	DIO5	RTS0			PWM1	PC1
8	10	8	DIO6	TXD0			PWM2	
9	3	9	DIO7	RXD0			PWM3	
10	11	14	DIO8	TIM0CK_GT	PC1		PWM4	
11	12	12	DIO9	TIM0CAP	32KTALIN	RXD1		
12	13	4	DIO10	TIM0OUT	32KTALOUT			
13	15	13	DIO12		PWM2	CTS0		
14	2	10	DIO14	SIF_CLK		TXD1	SPISEL1	
15	16	1	DIO13		PWM3	RTS0		
16	17	0	DIO11		PWM1	TXD1		
17	19	11	DIO15	SIF_D		RXD1	SPISEL2	
18	18	20	DIO16	COMP1P		SIF_CLK		
19	20	21	DIO17	COMP1M	PWM4	SIF_D		
20	1,14	GND	GND					
21	21	RESETN	RESETN	RESETN				
22	24	14	ADC2	VREF				
23	22	15	ADC1					
24	23	16	DIO0	SPISEL1	ADC3			
25	25	17	DIO1	SPISEL2	ADC4	PC0		
26	26	18	DIO2				TIM0CK_GT	
27	27	19	DIO3				TIM0CAP	
28	1,14	GND	GND	GND				
29		LA	N/A	N/A				
30	1,14	GND	GND	GND				
31	1,14	LB	GND	GND				
32	1,14	GND	GND	GND				

JN518x のペリフェラル割当

		SPI			USART		I2C		CT32		PDM											
PIO	PWM	ADC	0	1	0	1	0	1	B0	B1	0	1	COMP	SO781	RF,ANT	SerWire	SPIFI	MISC				
0	PWM0			SCK	SCK	TXD					DATA											
5			SSELN	MISO, SSELN	RTS										RFRX			ISP ENT				
2	PWM2		SCK	MOSI	RXD									RST				MCLK				
3	PWM3		MISO	SSELN0	TXD									CLK								
VCC																						
7	PWM7		MISO		CTS	RXD		SDA		MAT1					ADO							
6	PWM6		SCK		RTS	TXD		SCL		MAT0					ADE							
8	PWM8		MOSI		TXD				MAT0			DATA	OUT		RFTX							
9	PWM9		SSELN		RXD	SCK			CAP1			CLK			ADO							
14	PWM1	ADC0		SSELN1	SCK				CAP1						RFTX	SWO		MCLK				
12	PWM0		MOSI					SCL					OUT			SWCLK		IR BLASTER				
4	PWM4		MOSI	SSELN1	CTS									IO	RFTX			ISP SEL				
13	PWM2		SSELN	SSELN2				SDA								SWDIO						
10			SCK			TXD	SCL			CAP0		DATA			RFTX							
1	PWM1			MISO		RXD						CLK										
0	PWM0			SCK	SCK	TXD						DATA										
11			MISO			RXD	SDA			CAP0	CLK				RFRX							
20	PMW8					TXD							ACP		RFTX		IO2	IR BLASTER				
21	PWM9					SCK							ACM		RFRX	SWO	IO1	IR BLASTER				
GND																						
RESETN																						
14	PWM1	ADC0		SSELN1	SCK				CAP1						RFTX	SWO		MCLK				
15	PWM3	ADC1		SCK			SCL					DATA	OUT		RFRX							
16	PWM5	ADC2		SSELN0			SDA					CLK		RST			CSN					
17	PWM6	ADC3		MOSI										CLK		SWO	IO3	CLK OUT				
18	PWM7	ADC4		MISO	TXD				MAT1					IO			CLK					
19	PWM4	ADC5			RXD	RXD									ADO		IO0	CLK IN				
GND																						
LA																						
GND																						
LB																						
GND																						